Attorney Docket No. P19

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Satyadev R. Patel et al.

Art Unit:

2812

Serial No.:10/005,308

Examiner:

Not Yet Assigned

Filed: December 3, 2001

For: METHOD FOR PERFORMING A MICRO-ELECTROMECHANICAL DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97(b)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The attention of the Patent and Trademark Office is herby directed to the documents listed on the attached PTO form 1449. Copies of the cited references are enclosed.

This Information Disclosure Statement is being submitted after issuance of a first official action on the merits and expiration of the three month period following filing of the above-captioned application, but prior to issuance of either a final official action or a Notice of Allowance. The \$180 fee set forth in 37 CFR Section 1.17(p) is enclosed.

The above information is presented so that the Patent and Trademark Office can determine any materiality thereof to the claimed invention. It is respectfully requested that the information be expressly considered during the prosecution of this application.

10/29/2003 ZJUHAR1 00000067 10005308

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180.00 GP

The Commissioner is hereby authorized to charge any additional fee (or credit any overpayment) associated with this statement to our Deposit Account No. 501516.

Respectfully submitted,

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ATTY. DOCKET NO. SERIAL NO. **QRMATION DISCLOSURE P19-US** 10/005,308 **CITATION** APPLICANT Satayadev R. Patel, et al. PTO-1449 FILING DATE 12/03/01 **GROUP 2812 U.S. PATENT DOCUMENTS CLASS EXAMINER'S** PATENT NO. DATE NAME **SUBCLASS** FILING DATE INITIALS 06/06/00 6,072,236 Akram, et al. 5.915.168 06/22/99 Salatino, et al. FOREIGN PATENT DOCUMENTS TRANSLATION **SUBCLASS EXAMINER'S** PATENT NO. DATE COUNTRY **CLASS INITIALS** YES NO П OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Tom Glenn, et al., PACKAGING MICROSCOPIC MACHINES, MACHINE DESIGN, December 7, 2000. Balaji Sridharan, et al., POST-PACKAGING RELEASE A NEW CONCEPT FOR SURFACE MICROMACHINED DEVICES, Mechanical and Aerospace Engineering Department, 4 pgs. U. Gosele, et al., WAFER BONDING FOR MICROSYSTEMS TECHNOLOGIES, Sensors and Actuators 74 (1999) Pgs 161-168. Masao Segawa, et al., A CMOS INAGE SENSOER MODULE APPLIED FOR A DIGITAL STILL CAMERA UTILIZING THE TAB ON GLASS (TOG) BONDING METHOD, IEEE TRANSACTIONS ON ADVANCED PACKAGING, VOL 22., NO. 2. In-Byeong Kang, et al., THE APPLICATION OF ANISOTROPIC CONDUCTIVE FILMS FOR REALISATION OF INTERCONNECTS IN MICROMECHANICAL STRUCTURES, SPIE Vol. 3321, Pgs 233-238. Sonja van der Groen, et al., CMOS COMPATIBLE WAFER SCALE ADHESIVE BONDING FOR CIRCUIT TRANSFER, International Conference on Solid- State Sensors and Actuators, Chicago, June 16-19, 1997, Pgs 629-632. G. Blink, et al., WAFER BONDING WITH AN ADHESIVE COATING, Part of the SPIE Conference on Micromachined Devices and Components IV, Santa Clara, California, September 1998, Pgs 50-61. Christine Kallmayer, et al., A NEW APPROACH TO VHIP SIZE PACKAGE USING MENISCUS SOLDERING AND FPC-BONDING, IEEE TRANSACTIONS ON COMPONENTS PACKAGING AND MANUFACTURING TECHNOLOGY-PART C., VOL. 21, NO. 1., JANUARY 1998, Pgs 51-56. Joachim Kloeser, et al., LOW COST BUMPING BY STENCIL PRINTING: PROCESS QUALIFICATION FOR 200 UM PITCH, 1998 International Synposium on Microelectronics, 11 Pgs. Michel M. Maharbiz, et al., BATCH MICROPACKAGING BY CONPRESSION-BONDED WAFER-

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EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.





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This collection of information is required by 37 CFR 1.8. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.8 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.